

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	723	379/382.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 12:53
L2	1	L1 and differential adj ring\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 12:55
L3	758	379/418.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 12:55
L4	1	L3 and differential adj ring\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 13:02
L6	11	"379"/\$.ccls. and differential adj ringing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 13:13
L7	1174	379/413-413.01.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 13:12
L8	2	L7 and differential adj ringing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:48
L11	10358	(ringing adj signal\$1 or call adj alert\$3 or alert\$3 adj signal\$1) and \$4phone	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 13:18

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L12	2756	L11 and generat\$3 with (ringing adj signal\$1 or call adj alert\$3 or alert\$3 adj signal\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 13:19
L13	23	L12 and (ringing adj signal\$1 or call adj alert\$3 or alert\$3 adj signal\$1) with differential	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:31
L15	4	L13 and (DC\$1offset or DC adj offset)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:47
L17	38	L12 and (DC-offset or DC adj offset)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:22
L19	61	(george-scott\$.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:47
L20	2	L19 and differential adj ringing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:52
L21	248	silicon adj laborator\$.AS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:51
L22	0	L21 and differential adj ringing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:53

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L23	8044	330/251-261.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:54
L24	0	L23 and differential adj ringing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:54
L25	562	340/384.1.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:54
L26	0	L25 and differential adj ringing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:54
L27	1	"330"/\$.ccls. and differential adj ringing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:56
L28	0	"340"/\$.ccls. and differential adj ringing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/10 14:56

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waveshapes with **DC offset**. Loop start and ground start. trunks are supported, and an open ... **differential ringing** gain of the device is 100V/V. The circuit ...

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**Differential Ringing** Gain is measured with VRS = 0.795 V. RMS. for -100V devices, VRS = 0.663 V ... The balanced ringing waveform consists of zero **DC offset** ...

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... up to 95V peak supporting sinusoidal or trapezoidal waveshapes with **DC offset**.

... Ringing Mode Referenced to the **Differential Ringing** Amplitude. ...

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 IEE Conference  
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 IEEE  
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- ☐ **1. Process and temperature compensation in a 7-MHz CM oscillator**  
Sundaresan, K.; Allen, P.E.; Ayazi, F.;  
Solid-State Circuits, IEEE Journal of  
Volume 41, Issue 2, Feb. 2006 Page(s):433 - 442  
Digital Object Identifier 10.1109/JSSC.2005.863149  
AbstractPlus | Full Text: PDF(2448 KB) IEEE JNL  
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- ☐ **2. Differential ring oscillators with multipath delay stages**  
Mohan, S.S.; Chan, W.S.; Colleran, D.M.; Greenwood, S.F.  
Kouznnetsov, I.G.;  
Custom Integrated Circuits Conference, 2005. Proceedings  
18-21 Sept. 2005 Page(s):503 - 506  
Digital Object Identifier 10.1109/CICC.2005.1568716  
AbstractPlus | Full Text: PDF(315 KB) IEEE CNF  
Rights and Permissions
- ☐ **3. Simulations of GaAs voltage controlled differential ring**  
Ceperic, V.; Baric, A.;  
EUROCON 2003. Computer as a Tool. The IEEE Region 8  
Volume 1, 22-24 Sept. 2003 Page(s):151 - 155 vol.1  
AbstractPlus | Full Text: PDF(330 KB) IEEE CNF  
Rights and Permissions
- ☐ **4. A 7-MHz process, temperature and supply compensated in 0.25 /spl mu/m CMOS**  
Sundaresan, K.; Brouse, K.C.; U-Yen, K.; Ayazi, F.; Allen,  
Circuits and Systems, 2003. ISCAS '03. Proceedings of the  
Symposium on  
Volume 1, 25-28 May 2003 Page(s):I-693 - I-696 vol.1  
AbstractPlus | Full Text: PDF(343 KB) IEEE CNF  
Rights and Permissions

- ☐ **5. Jitter and phase noise in ring oscillators**  
Hajimiri, A.; Limotyrakis, S.; Lee, T.H.;  
Solid-State Circuits, IEEE Journal of  
Volume 34, Issue 6, June 1999 Page(s):790 - 804  
Digital Object Identifier 10.1109/4.766813  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(752 KB\)](#) [IEEE](#)  
[Rights and Permissions](#)
  
- ☐ **6. Simulation technique for noise and timing jitter in elect**  
Zhang, C.W.; Wang, X.Y.; Forbes, L.;  
Circuits, Devices and Systems, IEE Proceedings [see also I  
Circuits, Devices and Systems]  
Volume 151, Issue 2, 12 April 2004 Page(s):184 - 189  
Digital Object Identifier 10.1049/ip-cds:20040435  
[AbstractPlus](#) | Full Text: [PDF\(1393 KB\)](#) [IEEE JNL](#)
  
- ☐ **7. A multiple-probe approach for robust frequency domai**  
**simulation**  
Xiaochun Duan; Kartikeya Mayaram;  
Custom Integrated Circuits Conference, 2005. Proceedings  
18-21 Sept. 2005 Page(s):465 - 468  
Digital Object Identifier 10.1109/CICC.2005.1568707  
[AbstractPlus](#) | Full Text: [PDF\(176 KB\)](#) [IEEE CNF](#)  
[Rights and Permissions](#)
  
- ☐ **8. Phase noise in inverter-based & differential CMOS ring**  
Abidi, A.A.; Samadian, S.;  
Custom Integrated Circuits Conference, 2005. Proceedings  
18-21 Sept. 2005 Page(s):457 - 460  
Digital Object Identifier 10.1109/CICC.2005.1568705  
[AbstractPlus](#) | Full Text: [PDF\(416 KB\)](#) [IEEE CNF](#)  
[Rights and Permissions](#)
  
- ☐ **9. A built-in self-test scheme for differential ring oscillator**  
Dermentzoglou, L.; Tsiatouhas, Y.; Arapoyanni, A.;  
Quality of Electronic Design, 2005. ISQED 2005. Sixth Int  
Symposium on  
21-23 March 2005 Page(s):448 - 452  
Digital Object Identifier 10.1109/ISQED.2005.2  
[AbstractPlus](#) | Full Text: [PDF\(616 KB\)](#) [IEEE CNF](#)  
[Rights and Permissions](#)
  
- ☐ **10. Artificial neural network in modelling of voltage contro**  
**with jitter**  
Ceperic, V.; Baric, A.; Pejcinovic, B.;  
Electrotechnical Conference, 2004. MELECON 2004. Proc  
12th IEEE Mediterranean  
Volume 1, 12-15 May 2004 Page(s):347 - 350 Vol.1  
Digital Object Identifier 10.1109/MELCON.2004.1346871  
[AbstractPlus](#) | Full Text: [PDF\(467 KB\)](#) [IEEE CNF](#)  
[Rights and Permissions](#)
  
- ☐ **11. A 5-Gb/s 1/8-rate CMOS clock and data recovery circu**  
Jin Kyu Kwon; Tae Kwan Heo; Sang-Bock Cho; Sung Mi  
Circuits and Systems, 2004. ISCAS '04. Proceedings of the  
Symposium on  
Volume 4, 23-26 May 2004 Page(s):IV - 293-6 Vol.4  
[AbstractPlus](#) | Full Text: [PDF\(477 KB\)](#) [IEEE CNF](#)  
[Rights and Permissions](#)

- ☐ **12. Simulation of timing jitter in ring oscillators**  
 Zhang, C.W.; Forbes, L.;  
University/Government/Industry Microelectronics Sympos  
Proceedings of the 15th Biennial  
 30 June-2 July 2003 Page(s):356 - 359  
[AbstractPlus](#) | Full Text: [PDF\(304 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **13. 25 GHz inductorless VCO in a 45 GHz SiGe technology**  
 Saniei, N., Jr.; Djahanshahi, H.; Salama, C.A.T.;  
Radio Frequency Integrated Circuits (RFIC) Symposium,  
 8-10 June 2003 Page(s):269 - 272  
[AbstractPlus](#) | Full Text: [PDF\(308 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **14. Timing jitter in a 1.35-GHz single-ended ring oscillator**  
 Zhang, C.W.; Forbes, L.;  
Circuits and Systems, 2002. MWSCAS-2002. The 2002 45  
Symposium on  
 Volume 3, 4-7 Aug. 2002 Page(s):III-308 - III-311 vol.3  
[AbstractPlus](#) | Full Text: [PDF\(321 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **15. A low-phase-noise CMOS ring oscillator with differenti**  
**quadrature outputs**  
 Liang Dai; Harjani, R.;  
ASIC/SOC Conference, 2001. Proceedings. 14th Annual I  
 12-15 Sept. 2001 Page(s):134 - 138  
 Digital Object Identifier 10.1109/ASIC.2001.954686  
[AbstractPlus](#) | Full Text: [PDF\(472 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **16. Design of a 3.3 V high frequency CMOS VCO with an**  
**functionality**  
 Yun Cheol Han; Kwang Sub Yoon;  
Circuits and Systems, 2001. MWSCAS 2001. Proceedings  
2001 Midwest Symposium on  
 Volume 1, 14-17 Aug. 2001 Page(s):324 - 327 vol.1  
 Digital Object Identifier 10.1109/MWSCAS.2001.986178  
[AbstractPlus](#) | Full Text: [PDF\(162 KB\)](#) IEEE CNF  
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- ☐ **17. CMOS VCOs for frequency synthesis in wireless bioteli**  
 Betancourt-Zamora, R.J.; Lee, T.H.;  
Low Power Electronics and Design, 1998. Proceedings. 19  
Symposium on  
 10-12 Aug 1998 Page(s):91 - 93  
[AbstractPlus](#) | Full Text: [PDF\(240 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **18. PLL design for a 500 MB/s interface**  
 Horowitz, M.; Chan, A.; Cobrunson, J.; Gasbarro, J.; Lee,  
 Richardson, W.; Thrush, T.; Fujii, Y.;  
Solid-State Circuits Conference, 1993. Digest of Technical  
ISSCC., 1993 IEEE International  
 24-26 Feb. 1993 Page(s):160 - 161, 282  
 Digital Object Identifier 10.1109/ISSCC.1993.280015  
[AbstractPlus](#) | Full Text: [PDF\(528 KB\)](#) IEEE CNF

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- ☐ **19. Selectivity of spatial filters for surface EMG detection of anterior muscle**  
 Farina, D.; Arendt-Nielsen, L.; Merletti, R.; Indino, B.; Gr...  
Biomedical Engineering, IEEE Transactions on  
 Volume 50, Issue 3, March 2003 Page(s):354 - 364  
 Digital Object Identifier 10.1109/TBME.2003.808830  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(750 KB\)](#) | [IEEE Rights and Permissions](#)
  
- ☐ **20. An efficient and robust method for ring-oscillator simulation harmonic-balance method**  
 Xiaochun Duan; Mayaram, K.;  
Computer-Aided Design of Integrated Circuits and Systems Transactions on  
 Volume 24, Issue 8, Aug. 2005 Page(s):1225 - 1233  
 Digital Object Identifier 10.1109/TCAD.2005.850803  
[AbstractPlus](#) | Full Text: [PDF\(240 KB\)](#) | [IEEE JNL Rights and Permissions](#)
  
- ☐ **21. High speed differential voltage clamped current mode ring oscillator**  
 Jeong, D.Y.; Chae, S.H.; Song, W.C.; Cho, G.H.;  
Electronics Letters  
 Volume 33, Issue 13, 19 June 1997 Page(s):1102 - 1103  
[AbstractPlus](#) | Full Text: [PDF\(260 KB\)](#) | [IEEE JNL Rights and Permissions](#)
  
- ☐ **22. A 1.0 V 10.2 GHz CMOS frequency divider with differential locking**  
 Fujishima, M.; Amamoto, K.;  
Wireless Communication Technology, 2003. IEEE Topical Conference, 15-17 Oct. 2003 Page(s):164 - 165  
 Digital Object Identifier 10.1109/WCT.2003.1321470  
[AbstractPlus](#) | Full Text: [PDF\(279 KB\)](#) | [IEEE CNF Rights and Permissions](#)
  
- ☐ **23. A CMOS time-to-digital converter based on a ring oscillator radar**  
 Nissinen, I.; Mantyniemi, A.; Kostamovaara, J.;  
Solid-State Circuits Conference, 2003. ESSCIRC '03. Proc. European  
 16-18 Sept. 2003 Page(s):469 - 472  
 Digital Object Identifier 10.1109/ESSCIRC.2003.1257174  
[AbstractPlus](#) | Full Text: [PDF\(524 KB\)](#) | [IEEE CNF Rights and Permissions](#)
  
- ☐ **24. A unified eigenvalue theory for time-varying linear circuits**  
 Zhu, J.; Johnson, C.D.;  
Circuits and Systems, 1990. IEEE International Symposium on  
 1-3 May 1990 Page(s):1393 - 1397 vol.2  
 Digital Object Identifier 10.1109/ISCAS.1990.112391  
[AbstractPlus](#) | Full Text: [PDF\(400 KB\)](#) | [IEEE CNF Rights and Permissions](#)
  
- ☐ **25. Active GHz clock network using distributed PLLs**  
 Gutnik, V.; Chandrakasan, A.P.;  
Solid-State Circuits, IEEE Journal of  
 Volume 35, Issue 11, Nov. 2000 Page(s):1553 - 1560  
 Digital Object Identifier 10.1109/4.881199

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